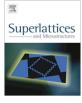


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# Rectifying characteristics of a Fe:SrTiO<sub>3</sub>/Nb:SrTiO<sub>3</sub> homojunction



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#### ABSTRACT

Fe-doped SrTiO<sub>3</sub> (Fe:STO) thin films have been deposited epitaxially on Nb-doped SrTiO<sub>3</sub> (Nb:STO) substrates to form a homojunction. Rectifying characteristics of the Fe:STO/Nb:STO homojunction are studied as a function of temperature. The current under reverse bias is observed to increase dramatically with decreasing temperature. This increased reverse current is ascribed to direct tunneling from the valence band of Fe:STO into the conduction band of Nb:STO, by taking into account the temperature and bias dependence of the dielectric constant of SrTiO<sub>3</sub>.

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### 1. Introduction

Oxide-electronics is an emerging area that has attracted great attention from both academia and electronic industry, owning to the broad spectrum of electronic properties that functional oxides have from superconductivity, metallic conduction, semiconductivity to insulation [1–3]. High carrier densities and short electronic length scales in oxides are desirable for device miniaturization [3]. Moreover, complex oxides also show ferroelectric, ferromagnetic, multiferroic, opto-electronic, piezo-electric and thermoelectric functionalities that provide agile coupling between different physical entities [3,4]. Oxide-electronics may be a valuable extension and addition to currently ubiquitous

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semiconductor electronics and has shown great potential in novel information storage and processing devices for future generations [5].

In oxide-electronics, perovskite SrTiO<sub>3</sub> (STO) may play an important role. Due to limited lattice mismatch, STO is a suitable substrate material for deposition of a number of important functional perovskites ranging from superconducting cuprates, ferroelectric titanates to colossal magnetoresistive manganites. Moreover, high-quality STO thin films can be grown epitaxially on silicon, thereby making STO an appropriate platform to integrate functional oxides and oxide electronic devices with silicon devices [6,7]. *n*-Type doping in STO can be achieved in Nb-doped STO (Nb:STO) by substituting Nb<sup>5+</sup> for Ti<sup>4+</sup>, or in La-doped STO by substituting La<sup>3+</sup> for Sr<sup>2+</sup> [8–10]. On the other hand, *p*-type STO can be obtained by replacing Ti<sup>4+</sup> with lower valence cations [11–14]. However, there are also arguments that *p*-type doping may be easily compensated in STO [15].

Junctions where different materials meet at the interface, form the most elementary devices in electronic circuits. Investigations on transport characteristics of oxide junctions are indispensable to make functional oxide electronic devices [16]. In this paper, we report temperature dependent transport characteristics of Fe:STO/Nb:STO homojunctions. It is observed that tunneling current becomes significant with decreasing temperature. This is discussed in terms of the decrease of junction width with decreasing temperature due to the unique temperature and bias dependence of the dielectric constant ( $\epsilon_r$ ) of STO.

# 2. Experimental

Epitaxial Fe:STO thin films were deposited on (001) Nb:STO (0.5 wt% Nb) substrates, 500 μm in thickness, by pulsed laser deposition (AdNaNo Corp.) from a single-crystalline Fe:STO (0.5 wt% Fe) target, using a Coherent CompexPro 205F KrF excimer laser with a 248 nm output at 4 Hz repetition. The energy density on target was estimated as ~1.5 J/cm². The deposition was performed under an oxygen pressure around 0.5 Pa and at a substrate temperature of 800 °C. X-ray diffraction (XRD) of the Fe:STO/Nb:STO junctions was performed at the BL14B1 beam line in Shanghai Synchrotron Radiation Facility. The X-ray wavelength used is 1.2378 Å. Surface morphology of the Fe:STO films was characterized using an Asylum Research Cypher atomic force microscope (AFM). Temperature-dependent transport measurements were performed using a Keithley 2636 source-measure unit on a LakeShore CRX-4K probe station. The voltage was applied on Fe:STO with Nb:STO grounded through indium contact pads.

#### 3. Results and discussions

Fig. 1(a) shows the XRD pattern of an Fe:STO thin films deposited on Nb:STO substrates around the (002) diffraction peak of Nb:STO. A strong diffraction peak from the (002) crystalline plane of the Fe:STO/Nb:STO heterostructure is observed with clear thickness fringes. Diffractions from Fe:STO cannot be distinguished from those from the Nb:STO substrate due to identical lattice constants. The slow damping of the intensity in thickness fringes demonstrates both a smooth film surface and a sharp interface. The film thickness, estimated from the period of the thickness fringes, is  $\sim$ 147 nm. Fig. 1(b) shows the surface AFM image of the Fe:STO film. Atomic steps with a height of  $\sim$ 0.4 nm, close to the height of a unit cell, can be observed. The root-mean-square roughness is estimated about 0.15 nm over a 3  $\mu$ m  $\times$  3  $\mu$ m area.

The transport characteristics of the Fe:STO/Nb:STO homojunction are shown in Fig. 2, as a function of temperature. At room temperature, a clear rectifying character is observed. The current increases with increasing forward bias rapidly, but keeps negligible with increasing reverse bias applied to Fe:STO up to -2.0 V. However, this rectifying character weakens with decreasing temperature as the reverse current increases dramatically. The inset shows  $\ln(I)$  versus eV/kT at various temperatures, which exhibits a linear relation at low forward voltages, in agreement with the empirical transport relation  $I = I_s \exp(eV/\eta kT)$ , where  $I_s$  is the saturation current under reverse bias and  $\eta$  the ideality factor [17]. At room temperature,  $\eta \approx 2.2$ , indicative of a recombination-dominated current. The recombination process, with  $\eta = 2$ , is well described by the Shockley–Read–Hall model [19], where electrons and holes are captured by a single defect level. However,  $\eta$  of the Fe:STO/Nb:STO junction increases

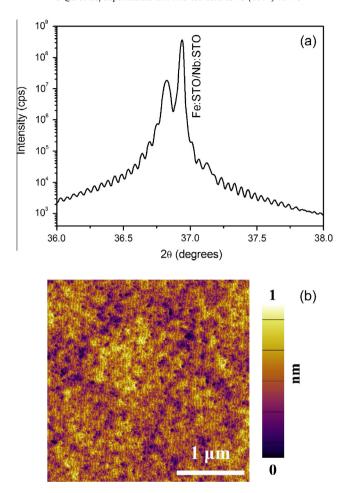


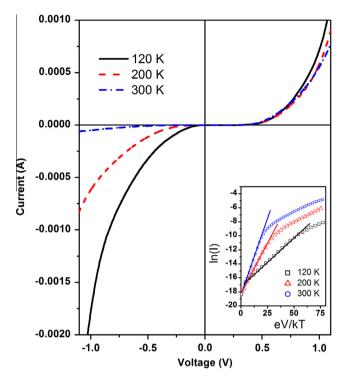
Fig. 1. (a) XRD pattern and (b) surface morphology of a Fe:STO thin film deposited on (001) Nb:STO substrate.

with decreasing temperature to about 3.6 at 200 K and about 7.0 at 120 K. Although an  $\eta$  value greater than 2 is often observed in p-n junctions [20,21], it cannot be explained by the Shockley-Read-Hall model. Instead, the high ideality factor may be ascribed to trap-assisted tunneling [22].

There are evidences that Fe-doping in STO behaves as acceptors [14,23], although only a part of Fe<sup>3+</sup> ions substitute Ti<sup>4+</sup> ions in the perovskite lattice [23]. The fermi level of Fe:STO gets closer to the valance band maximum as Fe concentration increases [24]. To explain the transport characteristics observed, we consider a p-n junction as shown in the inset of Fig. 3(a), taking into account the temperature and bias dependence of the dielectric constant of STO based on the analyses provided by Susaki and colleagues [18]. The dielectric constant of Fe:STO and Nb:STO is assumed to follow the same dependence. According to Susaki et al. [18]

$$\epsilon_r(E) = b / \sqrt{a + E^2},$$
 (1)

where  $a(T) = [b(T)/\epsilon_r(T,E=0)]^2$ ,  $b(T) = 1.37 \times 10^9 + 4.29 \times 10^7 T \text{ V/m}$  and E is the electric field.  $\epsilon_r(T,E=0)$  follows Barrett's formula [25],



**Fig. 2.** I-V curves of the Fe:STO/Nb:STO p-n junction at various temperatures.

$$\epsilon_r(T, E=0) = 1635 / \left[ \coth\left(\frac{44.1}{T}\right) - 0.937 \right]. \tag{2}$$

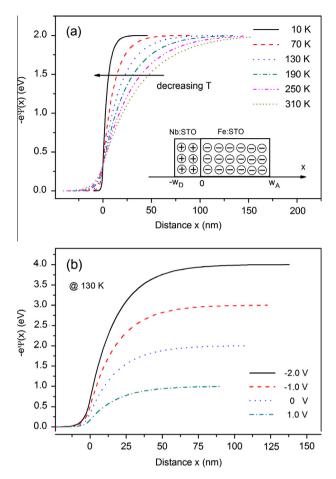
Taking  $\epsilon_r(E)$  into the Poisson equation, the electrostatic potential in the junction can be solved as,

$$\psi(x) = \begin{cases} \phi_{bi} - \frac{\sqrt{a}\epsilon_0 b}{\epsilon N_D} \left[ \cosh \frac{\epsilon N_D(x + w_D)}{\epsilon_0 b} - 1 \right] & -w_D < x \le 0 \\ \frac{\sqrt{a}\epsilon_0 b}{\epsilon N_A} \left[ \cosh \frac{\epsilon N_A(x - w_A)}{\epsilon_0 b} - 1 \right] & 0 < x < w_A, \end{cases}$$

$$(3)$$

where  $N_A$  and  $N_D$  are doping concentrations in Fe:STO and Nb:STO, respectively,  $w_A$  and  $w_D$  the depletion width in Fe:STO and Nb:STO, respectively, e the electron charge,  $\epsilon_0$  the dielectric constant of vacuum, and  $\phi_{bi} = \psi(-w_D) - \psi(w_A)$  is the build-in potential in the junction in equilibrium. The carrier concentration in Nb:STO is reported from  $10^{19}$  to  $10^{21}$  cm<sup>-3</sup> according to different Nb

The carrier concentration in Nb:STO is reported from  $10^{19}$  to  $10^{21}$  cm<sup>-3</sup> according to different Nb compositions and donor activation treatments [8,26,27]. A medium value  $N_D = 10^{20}$  cm<sup>-3</sup> is adopted in this work. There is little information in the literature on the carrier concentration in Fe:STO. However, due to the compensation in p-type STO [15], it is reasonable that  $N_A$  is much smaller than that in the Nb:STO substrate. We assume it to be  $2.0 \times 10^{19}$  cm<sup>-3</sup>. The fermi level of a lightly doped Nb:STO is reported 40 meV above the conduction band minimum [28]. First-principles calculations found that the Fe<sup>3+</sup> defect level is 1.1 eV above the valence band maximum [29]. The band gap of STO is about 3.2 eV [28]. Therefore, the build-in barrier height in the p-n junction  $-e\phi_{bi}$  is taken to be -2.0 eV for the calculations. Fig. 3 shows the junction energy barrier at various temperature and bias. Since  $N_D$  is much larger than  $N_A$ , the depleted space charge region falls almost completely in Fe:STO, the p-type region. It is clear that the depletion width decreases dramatically with decreasing temperature. At a certain temperature, 130 K as shown for example in Fig. 3(b), a forward bias increases the depletion width while a reverse bias decreases it.

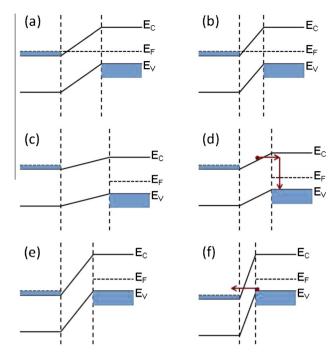


**Fig. 3.** Calculated energy barrier in the Fe:STO/Nb:STO p-n junction at various temperature (a) and bias (b). The inset in (a) shows a sketch of the junction used for the calculations. The origin of the coordination is set at the junction interface.

The low temperature characteristics, distinctly different from that of conventional semiconductors, stem from the unique temperature and bias dependence of  $\epsilon_r$  in STO. Susaki et al. [18] reported an increase of tunneling current contribution with decreasing temperature in a Au/Nb:STO Schottky junction, where the barrier width is significantly reduced at low temperatures due to the decrease of  $\epsilon_r$  in STO near the interface. The simulation results shown in Fig. 3 are in agreement with the previous report. Based on these, band diagrams in this STO p-n junction are proposed as shown schematically in Fig. 4. The key feature, in line with the above simulations, is the decrease in width of the depletion region at low temperatures. Under forward bias, diffusion and recombination currents are suppressed at low temperatures. But the decreased depletion width makes it easier for electrons to recombine through tunneling into defect levels in the band gap of Fe:STO. This results in an increase of  $\eta$  with decreasing temperature, as observed in Fig. 2. Under reverse bias, the decreased depletion width may facilitate direct tunneling from the valence band of Fe:STO into the conduction band of Nb:STO, similar to that in a backward diode [30].

## 4. Summary

In summary, we fabricated STO homojunctions by depositing Fe:STO thin films on top of Nb:STO substrates. Rectifying characteristics are observed at room temperature. With decreasing temperature,



**Fig. 4.** Schematic band diagrams of the Fe:STO/Nb:STO p-n junction at high temperature (a, c, e) and low temperature (b, d, f) in equilibrium (a, b), forward bias (c, d) and reverse bias (e, f). For clarity, the depletion region, between the two vertical dotted lines, is drawn completely in p-type Fe:STO.

the reverse current increases dramatically. This peculiar transport behavior are explained in terms of decreases depletion width with decreasing temperature. The decreased depletion width, which stems from the unique temperature and bias dependence of the dielectric constant of STO, increases the tunneling contribution under both forward and reverse bias.

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